



Form 1449 (Model 54d) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty. Docket No. NVIDP325 Applicant: Michael Johnson Filing Date: 09/27/2001	Application No.: 09/965,952 Group Art Unit: 2152- 2154
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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
✓✓	A	212,889	3/4/1879	Bridenthal Jr. et al.	n/a	n/a	n/a
	B	4,807,111	2/21/1989	Cohen et al.	364	200	6/16/1987
	C	4,839,851	6/13/1989	Maki	364	900	7/13/1987
	D	5,056,058	10/8/1991	Hirata et al.	364	900	3/5/1990
	E	5,163,131	11/10/1992	Row et al.	395	200	9/8/1989
	F	5,499,353	3/12/1996	Kadlec et al.	395	445	6/20/1995
	G	5,678,060	10/14/1997	Yokoyama et al.	395	831	10/28/1994
	H	5,991,299	11/23/1999	Radogna et al.	370	392	9/11/1997
	I	5,455,599	10/3/1995	Cabral et al.	345	133	4/7/1995
	J	5,544,357	8/6/1996	Huei	395	600	5/26/1995
	K	5,581,686	12/3/1996	Koppolu et al.	395	340	6/6/1995
	L	5,596,702	1/21/1997	Stucka et al.	395	340	4/16/1993
	M	5,621,434	4/15/1997	Marsh	345	145	8/11/1993
	N	5,650,941	7/22/1997	Coelho et al.	364	514	12/29/1995
	O	5,664,162	9/2/1997	Dye	345	521	11/3/1994
	P	5,680,605	10/21/1997	Torres	395	603	2/7/1995
	Q	5,734,852	3/31/1998	Zias et al.	395	334	3/8/1996
✓	R	5,754,556	5/19/1998	Ramseyer et al.	371	10.3	7/18/1996

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	S							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
✓	T	Muller, Raimund, "LON-das universelle Netzwerk Elektronik 22-1991
✓	U	Abbot, Mark B.; Peterson, Larry L., "Increasing Network Throughput by Integrating Protocol Layers" IEEE 1993
Examiner <i>V. ✓</i>		Date Considered <i>1-31-05</i>

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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	Filing Date:	Group Art Unit:
	09/27/2001	2152

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
✓✓	A	5,778,178	7/7/1998	Arunachalam	395	200.33	8/5/1996
	B	5,854,750	12/29/1998	Phillips et al.	364	478.04	9/3/1996
	C	5,870,622	2/6/1999	Gulick et al.	395	800.35	10/10/1997
	D	5,877,764	3/2/1999	Feitelson et al.	345	347	10/23/1992
	E	5,918,051	7/29/1999	Savitzky et al.	395	683	7/19/1995
	F	5,923,892	7/13/1999	Levy	395	800.31	10/27/1997
	G	5,946,487	8/31/1999	Dangelo	395	705	6/10/1996
	H	5,966,534	10/12/1999	Cooke et al.	395	705	6/27/1997
	I	5,968,161	10/19/1999	Southgate	712	37	2/7/1997
	J	6,078,736	6/20/2000	Guccione	395	500.17	8/28/1997
	K	5,485,460	1/16/1996	Schrier et al.	370	94.1	8/19/1994
	L	5,495,480	2/27/1996	Yoshida	370	60	6/21/1994
	M	5,566,170	10/15/1996	Bakke et al.	370	60	12/29/1994
	N	5,577,172	11/19/1996	Vatland et al.	395	114	8/31/1994
	O	5,577,237	11/19/1996	Lin	395	555	1/23/1995
	P	5,598,410	1/28/1997	Stone	370	469	12/29/1994
	Q	5,619,650	4/8/1997	Bach et al.	395	200.01	9/21/1995
	R	5,625,825	4/29/1997	Rostoker et al.	395	730	10/21/1993
	S	5,634,015	5/27/1997	Chang et al.	395	309	10/4/1994
	T	5,636,371	6/3/1997	Yu	395	500	6/7/1995
✓	U	5,663,951	9/2/1997	Danneels et al.	370	230	6/6/1995

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	V							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
✓✓	W	Wright, Maury, "Low-Cost Control LANs Add Automation to Homes, Autos, and Offices EDN-Technology July 20, 1992
Examiner ✓ ✓		Date Considered 1-31-05

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
W	A	5,687,314	11/11/1997	Osman et al.	395	200	6/9/1992
	B	5,696,899	12/9/1997	Kalwitz	395	200.1	11/18/1992
	C	5,699,350	12/16/1997	Kraslavsky	370	254	10/6/1995
	D	5,701,316	12/23/1997	Alferness et al.	371	53	8/31/1995
	E	5,727,149	3/10/1998	Hirata et al.	395	200.8	12/21/1995
	F	5,802,306	9/1/1998	Hunt	395	200.58	10/31/1995
	G	5,805,816	9/8/1998	Picazo, Jr. et al.	395	200.53	1/28/1997
	H	5,815,516	9/29/1998	Aaker et al.	371	53	4/5/1996
	I	5,826,032	10/20/1998	Finn et al.	395	200.66	2/12/1996
	J	5,872,919	2/16/1999	Wakeland	395	200.6	5/7/1997
	K	5,894,557	4/13/1999	Bade et al.	395	200.58	3/29/1996
	L	5,909,546	6/1/1999	Osborne	395	200.42	3/8/1996
	M	5,920,732	7/6/1999	Riddle	395	876	7/1/1996
	N	5,935,268	8/10/1999	Weaver	714	758	6/3/1997
	O	5,937,169	8/10/1999	Connery et al.	395	200.8	10/29/1997
	P	5,941,988	8/24/1999	Bhagwat et al.	713	201	1/27/1997
	Q	5,943,481	8/24/1999	Wakeland	395	200.6	5/7/1997
	R	5,974,518	10/26/1999	Nogradi	711	173	4/10/1997

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
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Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
W	T	Preston, David J., "Internet Protocols Migrate To Silicon For Networking Devices" Electronic Design April 14, 1997
Examiner V. W		Date Considered 1-31-05

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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
✓✓	A	5,999,974	12/7/1999	Ratcliff et al.	709	224	8/29/1997
	B	6,014,699	1/11/2000	Ratcliff et al.	709	224	8/29/1997
	C	5,012,489	4/30/1991	Burton et al.	375	8	11/7/1998
	D	5,161,193	11/3/1992	Lampson et al.	380	49	6/29/1990
	E	5,307,413	4/26/1994	Denzer	380	49	7/19/1991
	F	5,426,694	6/20/1995	Herbert	379	242	10/8/1993
	G	5,430,727	7/4/1995	Callon	370	85.13	5/19/1994
	H	5,440,551	8/8/1995	Suzuki	370	60	1/4/1994
	I	5,499,353	3/12/1996	Kadlec et al.	395	445	6/20/1995
	J	5,513,324	4/30/1996	Dolin, Jr. et al.	395	200.18	3/7/1994
	K	5,519,704	5/21/1996	Farinacci et al.	370	85.13	4/21/1994
	L	5,546,453	8/13/1996	Herbert	379	242	6/7/1995
	M	5,577,105	11/19/1996	Baum et al.	379	93	3/11/1994
	N	5,625,678	4/29/1997	Blomfield-Brown	379	93	5/24/1995
	O	5,636,371	6/3/1997	Yu	395	500	6/7/1995
	P	5,640,394	6/17/1997	Schrier et al.	370	389	1/16/1996
	Q	5,666,362	9/9/1997	Chen et al.	370	420	7/25/1995
	R	5,675,507	10/7/1997	Bobo, II	364	514	4/28/1995
	S	5,734,865	3/31/1998	Yu	395	500	6/27/1995
✓	T	5,748,905	5/5/1998	Hauser et al.	395	200.79	8/30/1996

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	U							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
✓✓	V	Chesson, Greg, "The Protocol Engine Project" Technology Focus September 1987
Examiner <i>✓ ✓</i>		Date Considered <i>1-31-05</i>

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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
✓✓	A	5,754,540	5/19/1998	Liu et al.	370	315	7/18/1995
	B	5,761,281	6/2/1998	Baum et al.	379	93.29	7/30/1996
	C	5,790,546	8/4/1998	Dobbins et al.	370	400	12/4/1995
	D	5,790,676	8/4/1998	Ganesan et al.	380	23	9/23/1997
	E	5,802,287	9/1/1998	Rostoker et al.	395	200.8	8/3/1995
	F	5,802,306	9/1/1998	Hunt	395	200.58	10/31/1995
	G	5,809,235	9/15/1998	Sharma et al.	395	200.6	3/8/1996
	H	5,818,935	10/6/1998	Maa	380	20	3/10/1997
	I	5,870,549	2/9/1996	Bobo, II	395	200.36	10/6/1997
	J	6,034,963	3/7/2000	Minami et al.	370	401	10/31/1996
	K	6,046,980	4/4/2000	Packer	370	230	11/24/1997
	L	6,049,857	4/11/2000	Watkins	711	207	7/23/1998
	M	6,061,368	5/9/2000	Hitzelberger	370	537	11/5/1997
	N	6,061,742	5/9/2000	Stewart et al.	709	250	10/10/1997
	O	6,076,115	6/13/2000	Sambamurthy et al.	709	250	4/24/1997
	P	6,081,846	6/27/2000	Hyder et al.	709	250	5/8/1997
	Q	6,092,110	7/18/2000	Maria et al.	709	225	10/23/1997
	R	6,092,229	7/18/2000	Boyle et al.	714	748	10/9/1996
↓	S	6,098,188	8/1/2000	Kalmanek, Jr. et al.	714	746	6/30/1995

Foreign Patent or Published Foreign Patent Application

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✓✓	U	Chesson, Greg, "Proceedings of the Summer 1987 USENIX Conference" USENIX Association June 8-12, 1987
✓✓	V	G. Chesson and L. Green, "XTP Protocol Engine VLSI for Real-Time LANS" EFOC/LAN June 29-July 1, 1968
Examiner V. ✓✓		Date Considered 1-31-05

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Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
✓✓	A	6,101,543	8/8/2000	Alden et al.	709	229	10/25/1996
	B	6,122,670	9/19/2000	Bennet et al.	709	236	10/30/1997
	C	6,151,625	11/21/2000	Swales et al.	709	218	4/30/1999
	D	6,157,955	12/5/2000	Narad et al.	709	228	6/15/1998
	E	6,172,980	1/9/2001	Flanders et al.	370	401	9/11/1997
	F	6,172,990	1/9/2001	Deb et al.	370	474	11/12/1997
	G	6,173,333	1/9/2001	Jolitz et al.	709	240	7/17/1998
	H	6,182,228	1/30/2001	Boden	713	201	8/17/1998
	I	6,185,619	2/6/2001	Joffe et al.	709	229	11/7/1997
	J	6,208,651	3/27/2001	Van Renesse et al.	370	392	6/9/1998
	K	6,226,680	5/1/2001	Boucher et al.	709	230	4/27/1998
	L	6,230,193	5/8/2001	Arunkumar et al.	709	218	10/31/1996
	M	6,233,626	5/15/2001	Swales et al.	710	11	10/6/1998
	N	6,233,626	5/15/2001	Swales et al.	710	11	10/6/1998
	O	6,247,060	6/12/2001	Boucher et al.	709	238	11/12/1999
	P	6,247,068	6/12/2001	Kyle	709	328	8/21/1997
	Q	6,327,625	12/4/2001	Wang et al.	709	235	11/30/1999
	R	6,330,659	12/11/2001	Poff et al.	712	34	11/6/1997
↓	S	6,341,129	1/22/2002	Schroeder et al.	370	354	4/3/1998

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
	T						Yes	No

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
✓✓	U	Wayner, "Sun Gambles on Java Chops", Bytes, Nov. 1996.
✓✓	V	Raz, "Real Time Program Language Accelerator", WO 98/21655, May 1998
Examiner <i>V. Ju</i>		Date Considered <i>1-31-05</i>

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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
W	A	6,345,301	2/5/2002	Burns et al.	709	230	3/30/1999
	B	6,341,129	1/22/2002	Schroeder et al.	370	354	4/3/1998
	C	6,347,347	2/12/2002	Brown et al.	710	23	12/17/1999
	D	6,389,479	5/14/2002	Boucher et al.	709	243	8/28/1998
	E	6,389,537	5/14/2002	Davis et al.	713	187	4/23/1999
	F	6,393,487	5/21/2002	Boucher et al.	709	238	3/12/2001
	G	6,629,141	9/30/2003	Elzur et al.	709	224	7/2/2002
	H	6,397,316	5/28/2002	Fesas, Jr.	711	200	7/24/1997
	I	6,427,169	7/30/2002	Elzur	709	224	7/30/1999
	J	6,430,628	8/6/2002	Conner	710	5	12/8/1998
	K	6,434,620	8/13/2002	Boucher et al.	709	2530	8/27/1999
	L	6,460,080	10/1/2002	Shah et al.	709	224	10/31/2001
	M	6,530,061	3/4/2003	Labatte	714	807	12/23/1999
	N	6,609,225	8/19/2003	Ng	714	781	12/21/2000
	O	2001/0021949	9/13/2001	Blightman et al.	709	219	5/14/2001
	P	2001/0023460	9/20/2001	Boucher et al.	709	250	12/26/2000
	Q	2001/0027496	10/4/2001	Boucher et al.	709	250	3/12/2001
	R	2001/0047433	11/29/2001	Boucher et al.	709	250	2/20/2001
	S	2002/0055993	5/9/2002	Shah et al.	709	223	10/31/2001
W	T	2002/0085562	7/4/2002	Hufferd et al.	370	392	12/12/2001

Foreign Patent or Published Foreign Patent Application

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							Yes	No
	U							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
W	V	Agrawal et al. "Architecture and Design of the Mars Hardware Accelerator", ACM 1987, pp. 101-107.
W	W	Case, "Implementing the Java Virtual Machine", Microprocessor Report, Mar. 1996.
Examiner <i>W, W</i>		Date Considered <i>1-31-05</i>

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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
✓✓	A	2002/0087732	7/4/2002	Boucher et al.	709	250	12/17/2001
	B	200200191844	7/11/2002	Craft et al.	709	230	10/2/2001
	C	2002/0095519	7/18/2002	Philbrick et al.	709	250	2/26/2002
	D	2002/0120899	8/26/2002	Gahan et al.	714	748	7/23/2001
	E	2002/0163888	11/7/2002	Grinfeld	370	235	3/7/2002
	F	2002/0095519	7/18/2002	Philbrick et al.	709	250	2/26/2002
	G	2002/0147839	10/10/2002	Boucher et al.	709	238	3/6/2002
	H	2003/0005142	1/2/2003	Elzur et al.	709	232	7/2/2002
	I	2003/0005143	1/2/2003	Elzur et al.	709	232	7/2/2002
	J	2003/0014544	1/16/2003	Petty	709	249	2/15/2001
	K	2003/0031172	2/13/2003	Grinfeld	370	389	5/30/2002
	L	2003/0016669	1/23/2003	Pfister et al.	370	392	7/12/2001
	M	2003/0046330	3/6/2003	Hayes	709	201	9/4/2001
✓	N	2003/0046418	3/6/2003	Raval et al.	709	237	8/31/2001

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
✓✓	O	WO 01/13583	2/22/2001	WIPO	H04L	12/28	Y	
✓✓	P	WO 01/28179	4/19/2001	WIPO	H04L	29/06	Y	
✓✓	Q	WO 02/059757	8/1/2002	WIPO	G06F	13/12	Y	
✓✓	R	WO 02/086674	10/31/2002	WIPO	H04J	3/22	Y	

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
✓✓	S	Kitadeya et al., "Matsushita Launches Web TV Internet Connection Terminal", http://www.mei.co.jp/corp/news/official.data/data.dir/en981112-1/en981112-1.html , Nov. 1998.
✓✓	T	iReady Product Data Sheet, Internet Tuner, <i>undated</i>
Examiner	<i>✓, ✓✓</i>	
Date Considered	<i>1-31-05</i>	

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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
W	A	2003/0056009	3/20/2003	Mizrachi et al.	709	245	9/6/2002
	B	2003/0058870	3/27/2003	Mizrachi et al.	370	395.52	9/6/2002
	C	2003/0061505	3/27/2003	Sperry et al.	713	200	8/30/2002
	D	2003/0066011	4/3/2003	Oren	714	758	4/11/2002
	E	2003/0084185	5/1/2003	Pinkerton	709	2369	10/30/2001
	F	2003/0095567	5/22/2003	Lo et al.	370	466	11/20/2001
	G	2003/0115350	6/19/2003	Uzrad-Nali et al.	709	231	12/14/2001
	H	2003/0167346	9/4/2003	Craft et al.	709	250	3/7/2001
	I	2003/0115417	6/19/2003	Corrigan	711	118	2/14/2002
	J	2003/0031172	2/13/2003	Grinfeld	370	389	5/30/2002
	K	2003/0128704	7/10/2003	Mizrachi et al.	370	394	8/28/2002
	L	2003/0145101	7/31/2003	Mitchell et al.	709	236	7/8/2002
	M	2003/0145270	7/31/2003	Holt	714	766	1/31/2002
	N	2003/0200284	10/23/2003	Philbrick et al.	709	219	4/14/2003

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
W	O	WO 02/39302	5/16/2002	WIPO	G06F	15/16		
	P	WO 03/021443	3/13/2003	WIPO	G06F	11/30		
	Q	WO 03/021447	3/13/2003	WIPO	G06F	12/00		
	R	WO 03/021452	3/13/2003	WIPO	G06F	13/00		
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W	W	Kelly, T., "Cheap Internet Hardware that Fits in Everything", ZDNet, http://www.zdnet.co.uk/news/1998/44/ns-5998.html , Nov. 1998
Examiner <i>W. W</i>		Date Considered <i>1-31-05</i>

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Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary)	Atty. Docket No.	Application No.:
	NVIDP325	09/965,952
	Applicant:	
	Michael Johnson	
	Filing Date:	Group Art Unit:
	09/27/2001	2152

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
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Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
W	C	AU 0070603	3/13/2001	AU	H04L	12/28		
	D	AU0228874	5/21/2002	AU	G06F	15/16		
	E	AU 723724	9/7/2000	AU	H04J	3/16		
	F	AU 734115	6/7/2001	AU	G06F	9/38		
	G	AU 0741089	11/22/2001	AU	H04M	11/06		
	H	AU 4435999	12/30/1999	AU	H04M	11/06		
	I	AU 4595297	5/22/1998	AU	H04J	3/16		
	J	AU 7364898	11/27/1998	AU	G06F	9/38		
	K	CA 2265692AA	5/7/1998	CA	H04J	3/16		N
	L	CA 2265692C	8/7/2001	CA	H04J	3/16		N
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	N	CA 2328829AA	12/16/1999	CA	H04M	11/06	Y	
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W	S	INCITS: Development work conducted in t10-I/O Interface-Lower Level September 30, 2002 Weber, Ralph O.
Examiner V. Vw		Date Considered 1-31-05

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✓	G	Wright, Maury, "Low-Cost Control LANs Add Automation to Homes, Autos, and Offices" EDN July 20, 1992
✓	H	Preston, David J., "Internet Protocols Migrate to Silicon For Networking Devices" Electronic Design April 14, 1997
✓	I	Muller, Raimund, "LON – das universelle Netzwerk" Elektronik 22/1991
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W	D	Steenkiste, Peter, "A High-Speed Network Interface for Distributed-Memory Systems: Architecture and Applications" ACM Transactions on Computer Systems, Vol. 15, No. 1 February 1997
W	E	Doumenis, Gr.A., Konstantoulakis, G.E., Reisis, D.I. and Stassinopoulos, G.I. "A Personal Computer Hosted Terminal Adapter For The Broadband Integrated Services Digital Network and Applications" National Technical University of Athens, Greece
W	F	Womack, Lucas; Mraz, Ronald; Mendelson, Abraham, "A Study of Virtual Memory MTU Reassembly withing the PowerPC Architecture" IEEE 1997
W	G	Steenkiste, Peter, "A Systematic Approach to Host Interface Design for High-Speed Networks" IEEE March 1994
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W	E	Clark, David D.; Romkey, John; Salwen, Howard, "An Analysis of TCP Processing Overhead" IEEE 1988
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U.S. Patent Documents

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✓	E	Bonjour, Dominique; de Hauteclouque, Gaelle; le Moal, Jacques, "Design and Application of ATM LAN/WAN Adapters" IEEE 1998
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	NVIDP325	09/965,952
	Applicant: Michael Johnson	Group Art Unit: 2152
Filing Date:	09/27/2001	

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✓	F	Huange, Jau-Hsiung; Chen, Chi-Wen, "On Performance Measurements of TCP/IP and its Device Driver" IEEE 1992
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	Applicant: Michael Johnson	
	Filing Date: 09/27/2001	Group Art Unit: 2152

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
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	B						Yes	No

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	NVIDP325	09/965,952
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Form 1449 (Modified)

Information Disclosure
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Atty. Docket No.

IREA0012

Applicant:

Johnson

Filing Date:

September 27, 2001

Serial No.:

09/965,952

Group:

2152 2157

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V. Vw

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